

**Notice of References Cited**

Application/Control No.

09/805,137

Applicant(s)/Patent Under

Reexamination

MOORE ET AL.

Examiner

A. M. Thompson

Art Unit

2825

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2003/0070013	04-2003	Hansson	710/59
	B	US-6,397,170	05-2002	Dean et al.	703/14
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	C-L. Su et al., Low Power Architecture Design and Compilation Techniques for High-Performance Processors, Compcon Sprin '94, Digest of Papers, pages 489-498, February 1994.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.